

The seL4® Verification Journey: How Have the Challenges and Opportunities Evolved

June Andronick Keynote of FMCAD 2022, Trento



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A good journey...



...starts with a dream





...delivers achievements





...should offer opportunities to reflect

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...and present a path to a bigger journey







#1 Make a dream come true: verified, performant kernel

#2

Deliver it to the world: true trustworthiness for critical software

> #3 Keep it live: for today and tomorrow







#1 Make a dream come true: verified, performant kernel







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Opportunities:

- achieve a decades-long dream
- demonstrate FM on real systems







Formally verified microkernel. At no more than 10% performance degradation. Gernot Heiser, ~2004

a research project wanting to solve a problem that was both







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> Done. Gerwin Klein & al, 2009

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Formally verified microkernel. At no more than 10% performance degradation. Gernot Heiser, ~2004

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And more. And more. Gerwin Klein & al. 2013

a research project wanting to solve a problem that was both





Minimised TCB!





Minimised TCB!







Minimised TCB!

microkernels

software hardware





Minimised TCB!

microkernels

software hardware





Minimised TCB!

microkernels

software hardware





Minimised TCB!

microkernels

/ performance?

software

hardware





Minimised TCB!

microkernels performance? hardware hardware





Minimised TCB!

microkernels performant microkernels!





Minimised TCB!







Minimised TCB!

microkernels performance? assurance? performant microkernels! performant

and verified microkernel





Minimised TCB!

microkernels performance? assurance? spec? performant microkernels! performant and verified

microkernel





Minimised TCB!

microkernels performance? assurance? spec? performant microkernels! and proved isolation performant

performant and verified microkernel





Minimised TCB!







Minimised TCB!









- Make formal verification scale to 10,000 lines of low-level code
- with proof frameworks supporting the verification of functional correctness, security properties and binary correctness
- while maintaining performance \bigcirc

- Combination of foundational techniques
 - Targeting machine-checked proof
- Working hand in hand with systems people



<text>



<text>



C Code



C Code

void kernel_call () {...}



seL4 kernel call graph

~10,000 LOC >500 functions




C-to-Isabelle Parser: C program → *SIMPL program*

(mainly) deeply embedded generic imperative language in Isabelle





abstract functional specification

kernel_call_A ≡ ... Specification C Code Semantics

kernel_call_body ≡ ...

























abstract functional specification





abstract *functional* specification

non-deterministic state monad with failure

state \rightarrow (res, state) set \times bool





 $C \equiv ADT uop kernel_call_body$





 $C \equiv ADT uop kernel_call_body$





 $C \equiv ADT uop kernel_call_body$

































no unauthorised modification (according to a user-level access-right policy)

After executing the kernel, the only modifications between the final state and the initial state must be authorised by the policy







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$\{\lambda s. s=s0\}$ kernel_call_A() { λ s. integrity p s s0}

Hoare triple

no unauthorised modification (according to a user-level access-right policy)

After executing the kernel, the only modifications between the final state and the initial state must be authorised by the policy









no unauthorised reading/leakage (according to a user-level access-right policy)





no unauthorised reading/leakage

(according to a user-level access-right policy)

Non-interference:

2 executions of the kernel from states that differ only on confidential info must reach states that only differ from confidential info

 $S \rightarrow S_1 \rightarrow S_2 \rightarrow S_3 \rightarrow \dots$ $t \rightarrow t_1 \rightarrow t_2 \rightarrow t_3 \rightarrow \dots$







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seL4 main theorem #4: binary verification



decompilation

translation validation



seL4 main theorems









Impact

Confidentiality Isabelle



Binary is correct w.r.t the spec and enforces isolation







Binary is correct w.r.t the spec and enforces isolation



World's most comprehensive mathematical proofs of correctness and security







#1 Make a dream come true: verified, performant kernel

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Challenges:

- Scale
- Thoroughness
- Performance

Solutions:

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#1 Make a dream come true: verified, performant kernel

Success!

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#2 Deliver it to the world: true trustworthiness for critical software







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Opportunities:

- used in products where it matters
- set a standard





Setting a standard

The practical advantages of program proving will eventually outweigh the difficulties, in view of the increasing costs of programming error.

Tony Hoare, 1969



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If the issue ever came to court, the defense of 'state-of-the-art' practice would always prevail.

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When total absence of error is a requirement (e.g., in aircraft control software or operating system security), failure to verify will be treated legally as negligence, as in other branches of engineering.

But this cannot happen until there is wide-ranging evidence of feasibility, cost, and tool support of experimental verification of realistic applications.

The sel4 microkernel is just the sort of demonstration that convinces.



The seL4 journey

Minimised TCB!






Minimised TCB!



Success!



Minimised TCB!

















































3rd party use in automotive medical space aviation military security industrial systems

 $\bullet \bullet \bullet$

QI FOUNDATION









General Members



RISC-V International

Premium Members: 6 General Members: 15 Associate Members: 5



Horizon Robotics

🕿 NIO

TU Munich

NIO

Membership: 26





Jump Trading





UNSW Sydney

Founding member Endorsed Service Provider



NCSC

3rd party use in automotive medical space aviation military security industrial systems

 $\bullet \bullet \bullet$

FOUNDATION











Success drives code evolution, code evolution requires proof evolution



Challenges:

- port verification to new platforms
- port verification to new features









Success drives code evolution, code evolution requires proof evolution

platform X?



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Success drives code evolution, code evolution requires proof evolution

Challenges:

- operation of the point of th
- port verification to new features







I want it all. And I want it now.









I want seL4 verified "with X on Y"

(It's usually what we don't have in stock :)

seL4-vanilla

seL4-MCS

MCS = Mixed-Criticality Systems





UNICORE

MULTICORE





















<u>"The" seL4 Theorem</u>











"The" seL4 Theorem(s)







"The" seL4 Theorem(s)





Then...



different configs



Arm 32-bit (non-MCS) (unicore)

different levels

Sel









seL4's formal proofs evolve with new <u>architectures</u>

seL4's formal proofs evolve with new <u>features</u>





(non-MCS) (unicore)









(non-MCS) (unicore)

Local Academic Academ





invariants



























x86 64-bit

Sel



Arm 32-bit









x86 64-bit







RISC-V 64-bit

Sel 4

👍 TS @ UNSW

HENSOLDT Cyber

Arm 32-bit

x86 64-bit





invariants



Sel Arm 32-bit

x86 64-bit

RISC-V 64-bit









Arm 64-bit (HYP!)



x86 64-bit

<u>seL4 proofs</u>

Done Ongoing Future

(non-MCS, unicore)

RISC-V 64-bit

seL4's formal proofs evolve with new <u>architectures</u>











seL4's formal proofs evolve with new <u>architectures</u>

seL4's formal proofs evolve with new <u>features</u>

The proofs have evolved with new features over the years



Two examples:

- bound notification endpoints
- bitfield scheduler optimisation

MCS is different:

- Mixed-Criticality Systems
- time as a resource
- large, invasive change



Big Feature: Mixed-Criticality Systems





Verification of multiple configs in parallel






#2 Deliver it to the world: true trustworthiness for critical software

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	Opportunities:
	• used in products where it matters
	set a standard
	Challenges:
	Challenges:o port verification to new platforms
	 Challenges: port verification to new platforms port verification to new features

More challenges:

- millions lines of proofs
- duplication



Some solutions



Some solutions







#3 Keep it live: for today and tomorrow







Any proof framework improvements is not easily usable on the MCS branch without duplicating work

Challenges:

- maintenance
- tech debt from deadlines
- application of improvements blocked

Solutions:

- robustness, automation, proof engineering
- consolidation







Roadmap



MCS can become the default configuration once all existing proofs completed on MCS

Eventually, seL4 verified on multicore, with unicore as an instance

Sel /







What would we have done differently, now that we know?

Opportunities to Reflect





What would we have done differently, now that we know?

Probably not much...

Opportunities to Reflect



I want it all. And I want it now.



"Doing arch-split too early would have killed the project"





Better

"Things could have been done differently *if* we had sorted out the right solution already"

Photo by Jim Tegman on Unsplash





I want it all. And I want it now.

Now

"Doing arch-split too early would have killed the project"





"a trade-off, everything is"

Better

Photo by Jim Tegman on Unsplash

"Things could have been done differently *if* we had sorted out the right solution already"





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Conclusion



Photo by Joshua Earle on Unsplash

Path to a bigger journey

seL4's formal proofs were a breakthrough in formal software verification

Success creates interest, interest pushes evolution

Formal proofs must evolve as the code evolves

Proofcraft is committed to keep this evolution alive





https://proofcraft.systems





