Differential Testing of Pushdown Reachability with a Formally Verified Oracle

<u>Anders Schlichtkrull</u> Morten Konggaard Schou Jiří Srba *Aalborg University*

Dmitriy Traytel University of Copenhagen



Finite automaton + Unbounded stack





 $\langle p_1, yx \rangle$



$$\left\langle p_1, yx \right\rangle \Rightarrow \left\langle p_1, xyx \right\rangle$$



$$\langle p_1, yx \rangle \Rightarrow \langle p_1, xyx \rangle \Rightarrow \langle p_2, yyx \rangle$$



$$\langle p_1, yx \rangle \Rightarrow \langle p_1, xyx \rangle \Rightarrow \langle p_2, yyx \rangle \Rightarrow \langle p_3, yx \rangle$$



$$\langle p_1, yx \rangle \Rightarrow \langle p_1, xyx \rangle \Rightarrow \langle p_2, yyx \rangle \Rightarrow \langle p_3, yx \rangle \Rightarrow \langle p_2, xx \rangle$$

Background: Pushdown Reachability

We are interested in reachability!

E.g. to check for safety:

Can a system get into some bad configuration?

Or more generally:

Let C be a set of initial configurations.

Let C' be a set of final configurations.

Is C' reachable from C? $\exists c \in C, c' \in C'. c \Rightarrow^* c'$

Applications:

- Interprocedural control-flow analysis of recursive programs
- Static analysis of Java, C and C++.
- Communication network analysis
- Analysis MPLS communication protocols.

Background: Reachability

• For set $C \subseteq P \times \Gamma^*$ of configurations define:

- Predecessors: $pre^*(C) = \{c \mid \exists c' \in C, c \Rightarrow^* c'\}$
 - All configurations that can reach C.
- Successors: $post^*(C) = \{c \mid \exists c' \in C, c' \Rightarrow^* c\}$

• All configurations that C can reach.

- C, pre*(C), and post*(C) can be infinite.
- Rephrasing reachability of C' from C:
 C ∩ pre * (C') ≠ Ø ?
 Is there a configuration that is initial and can reach the finals?
- For regular sets: We can calculate pre* and decide reachability!

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- C, pre*(C), and post*(C) can be infinite.
- Rephrasing reachability of C' from C:
 C' ∩ post * (C) ≠ Ø ?
 Is there a configuration that is final and that the initials can reach?
- For regular sets: We can calculate post* and decide reachability!

Background: *pre** **preserves regularity** [Büchi 1964]

For a regular set of configurations C, $pre^*(C)$ is regular. [Büchi 1964]

Saturation procedure in polynomial time[CONCUR'97, INFINITY'97]Algorithm with improved complexity[S. Schwoon 2002]Fast algorithm in practice[ATVA'21]

Add transitions to P-Automaton until saturation.

Background: *post** **preserves regularity** [Büchi 1964]

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Add transitions to P-Automaton until saturation.

Background: P-automata

P-automata are representations of regular sets of configuration. Here is an example:



Sets recognized by some \mathcal{P} -automaton are called regular.

For these sets pre* can be calculated! We will now look at why and how.



Set of configurations

C $lang(\mathcal{A}) = \{\langle p_2, yx \rangle, \langle p_3, xx \rangle\}$





Set of configurations

C
$$lang(\mathscr{A}) = \{\langle p_2, yx \rangle, \langle p_3, xx \rangle\}$$





This can be done in polynomial time.

PDAAAL

An implementation of pushdown reachability.

Used in particular for analysis of MPLS communication protocols.

Based on pre*, post* and a new algorithm dual*. [ATVA'21]

What if PDAAAL has a bug?

We want to avoid that!





What is the ideal tool to test against?



Oracle



With Isabelle/HOL!





















Formalized Correctness Theorem

We prove similar theorems for post* and dual*.

Total effort: 4400 Isabelle LOC. ~2 person months.

We followed [S. Schwoon 2002].

Lehrstuhl für Informatik VII der Technischen Universität München

Model-Checking Pushdown Systems

Stefan Schwoon

Vollständiger Abdruck der von der Fakultät für Informatik der Technischen Universität München zur Erlangung des akademischen Grades eines

Doktors der Naturwissenschaften (Dr. rer. nat.)

genehmigten Dissertation.

Vorsitzender: Univ.-Prof. T. Nipkow, Ph.D.

Prüfer der Dissertation: 1. Univ.-Prof. Dr. Dr.h.c. W. Brauer 2. Prof. Dr. J. Esparza,



What do we pick for the input?

What do we pick for the input?

Phase I

- Real world test from the network domain
- In total: 25 512 test cases.

Phase 2

- Randomly generated pushdown systems
 - 4 control locations, 5 labels, up to 200 pushdown rules, up to 13 automata transitions
- In total: 15 000 test cases.

Phase 3

- Exhaustive testing up to a small size
 - 2 control locations, 2 stack symbols, up to 2 pushdown rules, automata with respectively 2 and 1 initial state, up to 2 automata transitions
- In total: 27 000 000 test cases.







PDAAAL and the oracle agree!





~1500 test where they disagree!

Let's look at an example

$(p_1,B){\hookrightarrow}(p_1,swapC),$	$(p_2,B){\hookrightarrow}(p_2,swapE),$
$(p_1,E){\hookrightarrow}(p_1,swapC),$	$(p_2,E) \hookrightarrow (p_3, push A E),$
$(p_1,A){\hookrightarrow}(p_2,swapA),$	$(p_2,B) \hookrightarrow (p_3,push C B),$
$(p_1,D){\hookrightarrow}(p_2,\!swapD),$	$(p_3,D) \hookrightarrow$
$(p_1,C){\hookrightarrow}(p_2,swap\:E),$	$(p_0, push B D),$
$(p_1,C){\hookrightarrow}(p_3,\!swapD),$	$(p_3,C) \hookrightarrow (p_0,push \ E \ C),$
$(p_1,D) \hookrightarrow (p_3,pop),$	$(p_3,C){\hookrightarrow}(p_0,\!swapE),$
$(p_2,B) {\hookrightarrow} (p_0, push A B),$	$(p_3,C) \hookrightarrow (p_1,push A C),$
$(p_2,A){\hookrightarrow}(p_0,pushCA),$	$(p_3,B){\hookrightarrow}(p_1,pop),$
$(p_2,C){\hookrightarrow}(p_0,push\:C\:C),$	$(p_3,E){\hookrightarrow}(p_2,swapC),$
$(p_2,D) \hookrightarrow$	$(p_3,B) \hookrightarrow (p_2,push D B)$
$(p_0, push B D),$	$(p_3,E){\hookrightarrow}(p_3,\!swapA),$
$(p_2,C) \hookrightarrow (p_1,push \ C \ C),$	$(p_3,A) \hookrightarrow (p_3,push C A),$
$(p_2,A){\hookrightarrow}(p_1,push\:B\:A),$	$(p_3,E){\hookrightarrow}(p_3,\!swapD),$
$(p_2,A){\hookrightarrow}(p_2,push\:A\:A),$	$(p_3,C) \hookrightarrow (p_3,pop) \}$
$(p_2,C){\hookrightarrow}(p_2,\!swapA),$	
$(p_2,E){\hookrightarrow}(p_2,\!swapA),$	$A_1 ~=~ \{(Init ~ p_0, B,$
$(p_2,A){\hookrightarrow}(p_2,push\:B\:A),$	(Init p ₂ , B,
	$\begin{array}{l} (p_1,B) \hookrightarrow (p_1, \mathrm{swap}\ C),\\ (p_1,E) \hookrightarrow (p_1, \mathrm{swap}\ C),\\ (p_1,A) \hookrightarrow (p_2, \mathrm{swap}\ A),\\ (p_1,D) \hookrightarrow (p_2, \mathrm{swap}\ D),\\ (p_1,C) \hookrightarrow (p_2, \mathrm{swap}\ D),\\ (p_1,C) \hookrightarrow (p_3, \mathrm{swap}\ D),\\ (p_1,D) \hookrightarrow (p_3, \mathrm{pop}),\\ (p_2,B) \hookrightarrow (p_0, \mathrm{push}\ A\ B),\\ (p_2,A) \hookrightarrow (p_0, \mathrm{push}\ C\ A),\\ (p_2,C) \hookrightarrow (p_0, \mathrm{push}\ C\ C),\\ (p_2,D) \hookrightarrow\\ (p_0, \mathrm{push}\ B\ D),\\ (p_2,C) \hookrightarrow (p_1, \mathrm{push}\ C\ C),\\ (p_2,A) \hookrightarrow (p_1, \mathrm{push}\ C\ A),\\ (p_2,A) \hookrightarrow (p_2, \mathrm{push}\ A\ A),\\ (p_2,A) \hookrightarrow (p_2, \mathrm{push}\ A\ A),\\ (p_2,C) \hookrightarrow (p_2, \mathrm{swap}\ A),\\ (p_2,C) \hookrightarrow (p_2, \mathrm{swap}\ A),\\ (p_2,C) \hookrightarrow (p_2, \mathrm{swap}\ A),\\ (p_2,A) \hookrightarrow (p_2, \mathrm{swap}\ A),\\ (p_2,A) \hookrightarrow (p_2, \mathrm{push}\ B\ A),\\ (p_2, A) \hookrightarrow (p_2, p$

push C B), oush E C), swap E), oush A C), pop), swap C), push DB), swap A), push C A), swap D), pop)}

it p_0 , B, Noninit q_1), (Init p_0 , D, Noninit q_0), it p_2 , B, Noninit q_0), (Init p_3 , A, Noninit q_2), (Noninit q_0 , D, Noninit q_1), (Noninit q_2 , C, Noninit q_0) $F_1 = \{\}$ $F_1^{ni} = \{q_1\}$ $A_2 = \{(Init p_2, A, Noninit q_0)\}, (Init p_2, B, Noninit q_0)\}$ $F_2 = \{p_0, p_2\} F_2^{ni} = \{\}$

Let's look at an example

 $\Delta = \{ (p_0, D) \hookrightarrow (p_0, swap A), \\ (p_0, E) \hookrightarrow (p_0, push B E), \\ (p_0, D) \hookrightarrow \\ (p_0, push D D), \\ (p_0, D) \hookrightarrow (p_0, pop), \\ (p_0, D) \hookrightarrow (p_1, swap A), \\ (p_0, A) \hookrightarrow (p_1, push C A), \\ (p_0, E) \hookrightarrow (p_2, push A E), \end{cases}$

- $(p_0,B) \hookrightarrow (p_2, push D B),$ $(p_0,C) \hookrightarrow (p_2, swap D),$ $(p_0,E) \hookrightarrow (p_2, swap E),$ $(p_0,E) \hookrightarrow (p_3, push B E),$ $(p_0,C) \hookrightarrow (p_3, swap E),$ $(p_1,B) \hookrightarrow (p_0, swap C),$ $(p_1,D) \hookrightarrow (p_0, swap C),$ $(p_1,C) \hookrightarrow (p_0, swap B),$ $(p_1,C) \hookrightarrow (p_0, swap E),$
- $(p_1,B) \hookrightarrow (p_1, \text{swap C}), \qquad (p_2, (p_1,E) \hookrightarrow (p_1, \text{swap C})), \qquad (p_2, (p_1,A) \hookrightarrow (p_2, \text{swap A})), \qquad (p_3, (p_1,D) \hookrightarrow (p_2, \text{swap D})), \qquad (p_3, (p_1,C) \hookrightarrow (p_2, \text{swap D})), \qquad (p_3, (p_1,C) \hookrightarrow (p_3, \text{swap D})), \qquad (p_3, (p_1,D) \hookrightarrow (p_3, \text{swap D})), \qquad (p_3, (p_2,B) \hookrightarrow (p_0, \text{push A B})), \qquad (p_3, (p_2,A) \hookrightarrow (p_0, \text{push C A})), \qquad (p_3, (p_3$
- $(p_2,C) \hookrightarrow (p_0,push C C),$ $(p_2,D) \hookrightarrow$ $(p_0,push B D),$ $(p_2,C) \hookrightarrow (p_1,push C C),$ $(p_2,A) \hookrightarrow (p_1,push B A),$ $(p_2,A) \hookrightarrow (p_2,push A A),$ $(p_2,C) \hookrightarrow (p_2,swap A),$
- $(p_2, E) \hookrightarrow (p_2, swap A),$
- $(p_2,A) \hookrightarrow (p_2,push B A),$

- $\begin{array}{l} (p_2,B) \hookrightarrow (p_2, swap \ E), \\ (p_2,E) \hookrightarrow (p_3, push \ A \ E), \\ (p_2,B) \hookrightarrow (p_3, push \ C \ B), \\ (p_3,D) \hookrightarrow \\ (p_0, push \ B \ D), \\ (p_3,C) \hookrightarrow (p_0, push \ E \ C), \\ (p_3,C) \hookrightarrow (p_0, swap \ E), \\ (p_3,C) \hookrightarrow (p_1, push \ A \ C), \\ (p_3,B) \hookrightarrow (p_1, pop), \end{array}$
- $(p_3,E) \hookrightarrow (p_2,swap C),$ $(p_3,B) \hookrightarrow (p_2,push D B),$ $(p_3,E) \hookrightarrow (p_3,swap A),$ $(p_3,A) \hookrightarrow (p_3,push C A),$ $(p_3,E) \hookrightarrow (p_3,swap D),$ $(p_3,C) \hookrightarrow (p_3,pop) \}$

It's hard to diagnose the problem on big examples like this.

 $A_{1} = \{(\text{Init } p_{0}, B, \text{Noninit } q_{1}), (\text{Init } p_{0}, D, \text{Noninit } q_{0}), \\(\text{Init } p_{2}, B, \text{Noninit } q_{0}), (\text{Init } p_{3}, A, \text{Noninit } q_{2}), \\(\text{Noninit } q_{0}, D, \text{Noninit } q_{1}), (\text{Noninit } q_{2}, C, \text{Noninit } q_{0})\}$ $E_{1} = \{\} = \{P_{1}^{ni} = \{q_{1}\}\}$

$$F_1 = \{\}$$
 $F_1^{ni} = \{q_1\}$

 $A_2 = \{(Init p_2, A, Noninit q_0)), (Init p_2, B, Noninit q_0)\}$

$$F_2 = \{p_0, p_2\} F_2^{ni} = \{\}$$

Delta Debugging

- We implement Delta Debugging.
- Fully automatic minimization of failing test cases.
- A failing test case is seen as a set of features.
- Delta Debugging checks if some subset also fails.
- Does so until a minimal set is found.
- We fix the set of features to contain
 - (i) each pushdown rule,
 - (ii) each transition in either of the P-automata,
 - (iii) each final state in a P-automaton
 - (as opposed to it not being final).

Result

$$\Delta = \{(\mathbf{p}_0, D) \hookrightarrow (\mathbf{p}_0, \mathbf{pop})\}\$$

 $A_1 = \{(Init p_0, D, Noninit q_0), (Noninit q_0, D, Noninit q_1)\}$

 $\mathsf{F}_1 \ = \ \{\} \ \ \mathsf{F}_1^{\mathsf{ni}} \ = \ \{\mathsf{q}_1\}$

$$A_2 = \{\}$$

 $F_2 = \{p_0\} F_2^{ni} = \{\}$

What was the problem?

- We looked at a number of minimized failing test cases.
- Common trait: P-automaton A' accepted the empty word.
- The problem:
 - E-transitions were not handled correctly by PDAAALs intersection algorithm.

We fixed the bug and ran the tests.





PDAAAL and the oracle agree!





I test where they disagree!

We minimized the input. We diagnosed the problem.

It involves PDAAAL's implementation of early termination.

Given a PDA, a P-automaton A, and a P-automaton A':

- I. Calculate pre*(A')
- 2. Calculate intersection automaton: A \cap pre*(A')
- 3. If $A \cap pre^*(A')$ is non-empty

return "Reachable"

else

return "Non-reachable"

PDAAAL does not do 1., 2., 3. in sequence like this. Instead it follows this idea:

When an edge is added in the calculation of pre*(A'): Corresponding edges are added in calculation of intersection. Check if intersection is non-empty.

Fixing the bug

Algorithm: Automata transitions updated *before* nonemptiness check. In PDAAAL: Automata transitions updated *after* nonemptiness check.

Should happen before.

Commit fixing the problem:

✓ ⁺ 2 ■■□□□ src/include/pdaaal/Solver.h □□			
		@@ -119,10 +119,10 @@ namespace pdaaal {	
119	119	<pre>if (res.second) { // New edge is not already in edges (rel U workset).</pre>	
120	120	_workset.emplace(from, label, to);	
121	121	<pre>if (trace != nullptr) { // Don't add existing edges</pre>	
	122	<pre>+ _automaton.add_edge(from, to, label, trace_ptr_from<w>(trace));</w></pre>	
122	123	if constexpr (ET) {	
123	124	_found = _found _early_termination(from, label, to, trace_ptr_from <w>(trace));</w>	
124	125	}	
125		<pre>automaton.add_edge(from, to, label, trace_ptr_from<w>(trace));</w></pre>	
126	126	}	
127	127	}	
128	128	};	



PDAAAL and the oracle agree!



PDAAAL and the oracle agree!





Thousands of test where they disagree!

We minimized the input. We diagnosed the problem.

It involves PDAAAL's parser.

PDAAAL's parser

Assumes rules can be added without knowing labels in advance. Pushdown rule data structure

Assumes all labels are known in advance.

PDAAAL's parser

Pushdown rule data structure

Assumes rules can be added without knowing labels in advance.

Assumes all labels are known in advance.

A mismatch!

We fixed the bug and ran the tests.





PDAAAL and the oracle agree!





PDAAAL and the oracle agree!





PDAAAL and the oracle agree!

Conclusion

On the "theory level":

We formalized correctness of post*, pre* and dual*.

Thus PDAAAL is based on correct algorithms.

On the "implementation level":

A fully automatic **toolchain** for improving tools for pushdown reachability

- I. It does differential testing against our oracle.
- 2. It automatically minimizes counter examples using delta debugging.

The toolchain helped us find bugs in PDAAAL. We have fixed these bugs.



Performance

For the network test cases

The average CPU time (on AMD EPYC 7642 processors at I.5 GHz) per test case was 35 seconds for Isabelle, while PDAAAL used less than 0.02 seconds on most cases.

Execution

The execution of all tests in the three phases took 303 CPU days. We executed the tests on a compute cluster with I 536 CPU cores.